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*(Acts whose publication is obligatory)*

## COUNCIL REGULATION (EEC) No 3681/92

of 14 December 1992

temporarily suspending the autonomous Common Customs Tariff duty on certain industrial products (in the microelectronics and related sectors)

THE COUNCIL OF THE EUROPEAN COMMUNITIES,

Having regard to the Treaty establishing the European Economic Community, and in particular Article 28 thereof,

Having regard to the proposal from the Commission,

Whereas production of the products referred to in this Regulation is at present inadequate or non-existent within the Community and producers are thus unable to meet the needs of user industries in the Community;

Whereas it is in the Community's interest in certain cases to suspend the autonomous Common Customs Tariff duties only partially, particularly because of the existence of Community production, and in other cases to suspend them completely;

Whereas suspension of these autonomous duties shall be decided by the Community;

Whereas, taking account of the difficulties involved in accurately assessing the development of the economic situation in the sectors concerned in the near future, these suspension measures should be taken only temporarily, by fixing their period of validity by reference to the interests of Community production,

HAS ADOPTED THIS REGULATION:

*Article 1*

The autonomous Common Customs Tariff duties for the products listed in the table appearing in the Annex shall be suspended at the level indicated in respect of each of them.

These suspensions shall apply from 1 January to 30 June 1993.

*Article 2*

This Regulation shall enter into force on 1 January 1993.

This Regulation shall be binding in its entirety and directly applicable in all Member States.

Done at Brussels, 14 December 1992.

*For the Council*

*The President*

N. LAMONT

## ANNEX

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8471 93 51	*40	Drive-unit for rewritable optical phase change discs with an external diameter of 13,34 cm (5,25 inch), for use in the manufacture of peripheral storage units, with a total storage capacity not exceeding 1 gigabyte (a)	0
ex 8471 93 51	*50	Drive-unit for magneto-optical discs with an external diameter of 13,34 cm (5,25 inch), for use in the manufacture of peripheral storage units, with a total storage capacity not exceeding 2 gigabytes (a)	0
ex 8471 93 59	*10	Floppy-disc storage units	0
ex 8471 93 59	*20	Disc-storage unit, capable of parallel data-transfer via 1, 4, 5 or 6 channels at, respectively, a rate per second of 3,014 megabytes, 12,05 megabytes, 15,07 megabytes or 18,08 megabytes, comprising 8 rigid magnetic discs with an external diameter of 20,32 cm (8 inch) with a total storage capacity, unformatted, not exceeding 1 000,2 megabytes and incorporating a storage-module-drive (SMD) interface	0
ex 8473 30 10	*50	Assembly for disc storage units of Winchester technology, comprising a 2- or 4-channel read/write monolithic integrated circuit for magnetic head signals mounted with discrete components on a flexible printed circuit attached to a metal bracket	0
ex 8473 30 90	*15	Metal ball coated with plastic or rubber material, with a diameter not exceeding 26 mm and a weight of 38 g or more, for use in the manufacture of a pointing device (so-called 'mouse') (a)	0
ex 8473 30 90	*35	Mechanical encoder, consisting of a disc, contact-brushes, a bearing, a spacer and a rotational shaft, mounted in a casing with 3 connections, and having a withstanding voltage of 50 V AC for at least 1 minute	0
ex 8501 10 99	*52	DC motor, brushless, with a typical running torque not exceeding 0,20 Nm, with a coupling flange of a diameter of 76 mm ( $\pm 1$ mm), with an outside rotor of a diameter not exceeding 55 mm ( $\pm 1$ mm), a four-phase winding, a rated speed of 3 600 ( $\pm 1$ %) rpm, a supply voltage of 12 V ( $\pm 10$ %) and fitted with wires and connectors	0
ex 8501 10 99	*68	DC motor, brushless, with a typical running torque not exceeding 0,0005 Nm, with a chuck of a diameter of 20 mm ( $\pm 0,5$ mm), a rated speed of 3 486 ( $\pm 0,1$ %) rpm and a supply voltage of 5 V ( $\pm 10$ %), mounted on a support the dimensions of which do not exceed 71 x 103 mm, fitted with a connector and conductor elements	0
ex 8501 10 99	*69	DC motor, brushless, with a typical running torque not exceeding 0,002 Nm, with a coupling flange of a diameter of 27,5 mm ( $\pm 0,5$ mm), a coupling flange of a diameter of 15 mm ( $\pm 0,5$ mm) and a chuck of a diameter of 25 mm ( $\pm 0,5$ mm), with an internal rotor, a three-phase winding, a rated speed of 3 409 ( $\pm 0,1$ %) rpm and a supply voltage of 12 V ( $\pm 5$ %)	0
ex 8501 10 99	*70	DC motor, brushless, with a typical running torque not exceeding 0,001 Nm, with a coupling flange of a diameter of 57 mm ( $\pm 0,5$ mm), a coupling flange of a diameter of 43,5 mm ( $\pm 0,5$ mm) and a chuck of a diameter of 25 mm ( $\pm 0,5$ mm), with an internal rotor, a three-phase winding, a rated speed of 3 575 ( $\pm 0,1$ %) rpm and a supply voltage of 12 V ( $\pm 5$ %)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8501 10 99	*71	DC bipolar motor, brushless, with a typical running torque not exceeding 0,0223 Nm, with a coupling flange of a diameter of 17,8 mm ( $\pm 0,5$ mm) and a chuck of a diameter of 25 mm ( $\pm 0,5$ mm), with an internal rotor, a three-phase winding, a rated speed of 4 498 ( $\pm 0,1$ %) rpm and a supply voltage of 12 V ( $\pm 10$ %)	0
ex 8501 10 99	*72	DC bipolar motor, brushless, with a typical running torque not exceeding 0,00825 Nm, with a coupling flange of a diameter of 27,4 mm ( $\pm 0,5$ mm) and a chuck of 25 mm ( $\pm 0,5$ mm), with an internal rotor, a three-phase winding, a rated speed of 4 498 ( $\pm 0,1$ %) rpm and a supply voltage of 12 V ( $\pm 10$ %)	0
ex 8501 10 99	*73	DC motor, brushless, with a typical running torque not exceeding $1,6 \times 10^{-5}$ Nm, with a coupling flange of a diameter of 32 mm ( $\pm 0,5$ mm), a chuck of a diameter of 25 mm ( $\pm 0,5$ mm), a rated speed of 3 663 ( $\pm 0,5$ %) rpm and a supply voltage of 12 V ( $+10$ % / $-8$ %), integrated on an aluminium baseplate the exterior dimensions of which do not exceed 98 x 141 mm	0
ex 8501 10 99	*74	DC motor, brushless, with a typical running torque not exceeding 0,0071 Nm, with a coupling flange of a diameter of 12,7 mm ( $\pm 0,5$ mm) and a spindle of a diameter of 39,7 mm ( $\pm 1$ mm), with a rated speed of 3 600 ( $\pm 0,1$ %) rpm and a supply voltage of 11 V ( $\pm 10$ %)	0
ex 8501 10 99	*75	DC motor, brushless, with a typical running torque not exceeding 0,002 Nm, with a coupling flange of a diameter of 27,5 mm ( $\pm 0,5$ mm), a coupling flange of a diameter of 15 mm ( $\pm 0,5$ mm) and a chuck of a diameter of 25 mm ( $\pm 0,5$ mm), with an internal rotor, a three-phase winding, a rated speed of 3 857 ( $\pm 0,1$ %) rpm and a supply voltage of 12 V ( $\pm 5$ %)	0
ex 8517 90 98	*02	Assembly consisting of: — a flat screen monochrome cathode-ray tube with a diagonal measurement of the screen of 100 mm or more but not exceeding 110 mm and equipped with a deflector yoke, and — a printed circuit on which are mounted a deflection unit, a video-amplifier and a transformer, the whole mounted on a chassis, for the manufacture of video entry-phones (a)	0
ex 8522 90 99	*91	Single cassette-deck sub-assembly with a total thickness not exceeding 53 mm, for use in the manufacture of sound recording and reproducing apparatus (a)	0
ex 8529 90 98	*91	Device consisting only of an interline charge-coupled image sensor with filter, the dimensions of which do not exceed 43 x 59 mm	0
ex 8529 90 98	*92	Surface acoustic wave filter unit for a frequency of 45,5 MHz ( $\pm 50$ kHz) and a bandwidth of 3 dB at 340 kHz or of 20 dB at 950 kHz, contained in a housing the exterior dimensions of which do not exceed 5 x 13 x 21 mm, with not more than 6 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 45.5A or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8529 90 98	*93	Voltage-controlled oscillator, having an oscillation-frequency of 915 MHz or more at 4,5 V and not exceeding 890 MHz at 1 V, contained in a housing the exterior dimensions of which do not exceed 5 × 10 × 15 mm, with not more than 4 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: VW995S05 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8531 20 90	*10	Dot matrix display consisting of a line of 8 characters, each character composed of 35 light-emitting diodes (LEDs), comprising electronic components for interface and drive functions, contained in a housing the exterior dimensions of which do not exceed 20 × 43 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: HDSP 2107      HDSP 2112      PDSP 2110      PDSP 2112 HDSP 2111      HDSP 2113      PDSP 2111      PDSP 2113 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8531 20 90	*20	Active monochrome liquid crystal display (LCD), consisting of a layer of liquid crystals between two glass sheets or plates with 256 000 dots or more, mounted on a printed circuit board comprising electronic components providing drive and/or control functions	0
ex 8531 20 90	*30	Passive liquid crystal display (LCD), consisting of a layer of liquid crystals between two glass sheets or plates with 256 000 dots or more, mounted on a printed circuit board comprising electronic components providing drive and/or control functions	0
ex 8534 00 90	*93	Printed circuit on one or both sides of a ceramic substrate, consisting of conductor elements, contacts and resistors, incorporating connections isolated in vitrified layers, the dimensions of which do not exceed 45 × 45 mm, with not more than 550 connections	0
ex 8536 50 19 ex 8536 50 90	*91 *91	Reed switches in the form of a glass capsule containing a small quantity of mercury and not more than 3 electrical contacts on metal arms	0
ex 8540 30 10	*34	Colour cathode-ray tube with a dot mask, equipped with electron guns placed side by side (in-line technology), with a diagonal measurement of the screen of more than 72 cm and a distance of less than 0,5 mm between dots of the same colour	0
ex 8540 30 10	*35	Colour cathode-ray tube with a dot mask, equipped with electron guns placed side by side (in-line technology), having a diagonal measurement of the screen not exceeding 72 cm	0
ex 8540 30 90	*26	Flat screen monochrome cathode-ray tube, with a diagonal measurement of the screen of 142 mm or more but not exceeding 190 mm, a luminescence of 300 lumen or more but not exceeding 2 000 lumen, a resolution of 0,06 mm or more but not exceeding 0,1 mm, phosphor types P1 or P22 or P53 or P55 or P56, an anode voltage of more than 34 kV, a focus voltage of more than 7 kV and a cathode current of 3 mA or more	0
ex 8540 91 00	*91	Deflector yoke for cathode-ray tubes with an operating frequency of 31 250 Hz or more but not exceeding 64 000 Hz, incorporating a quadrupolar magnet	0
ex 8540 99 00	*91	Anode, cathode or output part, for the manufacture of magnetrons of subheading 8540 41 00 (a)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8541 29 90	*30	Field-effect transistor (FET), having a drain-to-source breakdown-voltage of 50 V, operating with a drain-current of 8,2 A, a drain-to-source resistance not exceeding 0,2 ohm, and with a dissipation rate not exceeding 25 W, contained in a housing the exterior dimensions of which do not exceed 6 × 7 mm, with not more than 3 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: IRFR 010 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 40 10	*70	Light-emitting diode (LED), made from aluminium-gallium-arsenic (AlGaAs) semiconductor, having a square base with an edge length not exceeding 8,2 mm, having a primary lens	0
ex 8541 40 10	*80	Light-emitting diode (LED) of Transparent Substrate (TS) technology, made from aluminium-gallium-arsenic (AlGaAs) semiconductor, having a luminous intensity of 1,4 candela or more at 20 mA	0
ex 8541 60 00	*93	Piezo-electric crystal oscillating at a frequency of 4 MHz or more but not exceeding 60 MHz, contained in a housing the exterior dimensions of which do not exceed 8 × 13 mm and with not more than 4 connections	0
ex 8542 11 01	*30	Wafer, not yet cut into chips, consisting only of static random-access memories (S-RAMs) with a storage capacity of 1 Mbit, for the manufacture of goods of subheading 8542 11 25 contained in a housing the exterior dimensions of which do not exceed 14 × 42 mm, with not more than 32 connections and bearing: — an identification marking consisting of or including the following combination of figures: 431000 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 01	*40	Wafer, not yet cut into chips, consisting only of microprocessors with an arithmetic-logic unit (ALU) with a processing capacity of 16 bits and an 8-bit data-bus, comprising a random-access memory (RAM) with a storage capacity not exceeding 2 Kbits, for the manufacture of goods of subheading 8542 11 64 contained in a housing the exterior dimensions of which do not exceed 18 × 43 mm, with not more than 64 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: 78C10 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 01	*50	Wafer, not yet cut into chips, consisting only of microcontrollers or microcomputers with an arithmetic-logic unit (ALU) with a processing capacity of 16 bits and an 8-bit data-bus, comprising a read only memory, non-programmable (ROM) with a storage capacity not exceeding 128 Kbits and a random-access memory (RAM) with a storage capacity not exceeding 2 Kbits, for the manufacture of goods of subheading 8542 11 73 contained in a housing the exterior dimensions of which do not exceed 18 × 43 mm, with not more than 68 connections and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 01 (cont'd)		<p>— an identification marking consisting of or including one of the following combinations of figures and letters: 78C11                      78C12                      78C14</p> <p>or</p> <p>— other identification markings relating to devices complying with the abovementioned description (a)</p>	0
ex 8542 11 01	*60	<p>Wafer, not yet cut into chips, consisting only of microcontrollers or microcomputers with a processing capacity of 4 bits, comprising a read only memory, non-programmable (ROM) or a programmable, non-erasable, read only memory (PROM) with a storage capacity not exceeding 128 Kbits and a random-access memory (RAM) with a storage capacity not exceeding 2 Kbits, for the manufacture of goods of subheading 8542 11 71 contained in a housing the exterior dimensions of which do not exceed 15 × 52 mm, with not more than 80 connections and bearing:</p> <p>— an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 7507                      75304                      75308                      75316                      75P316 7508                      75306                      75312                      75P308</p> <p>or</p> <p>— other identification markings relating to devices complying with the abovementioned description (a)</p>	0
ex 8542 11 05	*20	<p>Driver circuit for liquid crystal displays (LCDs) of C-MOS technology, with 40 or more output channels, and having a drive voltage of more than 10 V, in the form of a monolithic integrated circuit not contained in a housing (microchip), for the manufacture of liquid crystal display (LCD) modules (a)</p>	0
ex 8542 11 14	*02	<p>Dual or triple port dynamic random-access memory (D-RAM), with data registers and a serial read output control, with a storage capacity of 1 Mbit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 20 × 51 mm, with not more than 52 connections and bearing:</p> <p>— an identification marking consisting of or including one of the following combinations of figures and letters: TC 524256                      TC 528128                      MT 42 C 4256                      MT 43 C 8128 TC 524257                      MSM 442256                      MT 43 C 4257                      TMS 44 C 251 TC 528126                      MB 81 C 4251                      MT 43 C 4258</p> <p>or</p> <p>— other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 11 16	*02	<p>Dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a storage capacity of 256 K × 16 bits and an access time not exceeding 80 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 27 mm, with not more than 40 connections and bearing:</p> <p>— an identification marking consisting of or including one of the following combinations of figures and letters: MT 4C16256                      PD 424260                      PD 424170</p> <p>or</p> <p>— other identification markings relating to devices complying with the abovementioned description</p>	10

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 18	*01	<p>Dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a storage capacity of 256 K × 18 bits and an access time not exceeding 80 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 27 mm, with not more than 40 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: PD 424280</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	10
ex 8542 11 21	*01	<p>Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 16 × 4 bits and an access time not exceeding 35 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 9 × 21 mm, with not more than 16 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: CY27S03                      CY54S189                      CY7C189 CY27S07                      CY74S189                      CY7C190</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 21	*11	<p>Static random-access memory (S-RAM) with a storage capacity of 256 bits superimposed bit-for-bit on an electrically erasable, programmable, read only memory (E<sup>2</sup>PROM), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 24 mm, with not more than 18 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: X2210                      X2443                      X2444</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 21	*21	<p>Static random-access memory (S-RAM) with a storage capacity of 1 Kbit, superimposed bit-for-bit on an electrically erasable, programmable, read only memory (E<sup>2</sup>PROM), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 33 mm, with not more than 24 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: X 2001                      X 2201 A                      X 2212</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 21	*22	<p>Static random-access memory (S-RAM) with a storage capacity of 2 Kbits, superimposed bit-for-bit on an electrically erasable, programmable, read only memory (E<sup>2</sup>PROM), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 28 connections and bearing:</p>	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 21 (cont'd)		<ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: X 2002</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 21	*27	<p>Static random-access memory (S-RAM) with a storage capacity of 4 Kbits, superimposed bit-for-bit on an electrically erasable, programmable, read only memory (E<sup>2</sup>PROM), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 28 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: X 2004</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 21	*37	<p>Non-volatile static random access memory of C-MOS technology (C-MOS non-volatile S-RAM), with a capacity of 16 Kbits and an internal energy source, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 19 × 40 mm, with not more than 28 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: DS 1220 Y                      MK 48 Z 02</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	7
ex 8542 11 23	*20	<p>Dual port static random-access cache memory of C-MOS technology (C-MOS dual port S-Cache-RAM), with a storage capacity of 256 Kbits, comprising a bus-memory control circuit, a bus control circuit of a central processing unit (CPU), an address-latch and 2 multiplexers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 45 × 45 mm, with not more than 84 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: 82490XP</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 23	*21	<p>Non-volatile static random-access memory of C-MOS technology (C-MOS non-volatile S-RAM), with a storage capacity of 256 Kbits and an internal energy source, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 19 × 40 mm, with not more than 28 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: BQ 4011                      DS 1230</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0



CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 23	*23	<p>Static random access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 32 K × 8 bits and an access time exceeding 55 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 32 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: HM 62256    HY 62C256    MB 84256    PD 43256    TC 55257</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 23	*25	<p>Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 64 K × 4 bits and an access time not exceeding 15 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 9 × 30 mm, with not more than 32 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: MCM 6208                    MCM 62980                    MCM 62983</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 25	*01	<p>Static random-access memory of BiMOS technology (BiMOS S-RAM), with a storage capacity of 288 Kbits and an access time not exceeding 40 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 9 × 41 mm, with not more than 32 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: TC 55B329</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 25	*10	<p>Static random-access memory (S-RAM), with a storage capacity of 1 Mbit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 47 mm, with not more than 44 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: CXX581020            CY7C108            HM 624257            TC 551001 CY7C101            CY7C109            M5M 51004            TC 55B4256 CY7C102            EDI 88128            M5M 51008            TC 55B4257 CY7C106            HM 621100A            MCM 6228            TC 55B8128 CY7C107            HM 624256            MT 5C1008</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 25	*11	<p>Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 512 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 28 mm, with not more than 40 connections and bearing:</p>	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 25 (cont'd)		<ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: TC 551632</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 25	*12	<p>Non-volatile static random-access memory of C-MOS technology (C-MOS non-volatile S-RAM), with a storage capacity of 1 Mbit and an internal energy source, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 19 × 44 mm, with not more than 32 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: BQ 4013</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 27	*01	<p>Non-volatile static random-access memory of C-MOS technology (C-MOS non-volatile S-RAM), with a storage capacity of 2 Mbits and an internal energy source, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 19 × 54 mm, with not more than 40 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: BQ 4014                      BQ 4024</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 27	*02	<p>Non-volatile static random-access memory of C-MOS technology (C-MOS non-volatile S-RAM), with a storage capacity of 4 Mbits and an internal energy source, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 19 × 54 mm, with not more than 40 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: BQ 4015                      BQ 4025</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 31	*06	<p>Read only memory, non-programmable of C-MOS technology (C-MOS ROM) with a storage capacity of 1 Mbit, comprising a serial data output port, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 43 mm, with not more than 32 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: TMS 60C80</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 31	*11	<p>Read only memory, non-programmable of C-MOS technology (C-MOS ROM), with a storage capacity of 8 Mbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 56 mm, with not more than 64 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters:            PD 23C8000            TC 538200            or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 31	*16	<p>Read only memory, non-programmable of C-MOS technology (C-MOS ROM), with a storage capacity of 16 Mbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 14 × 56 mm, with not more than 44 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters:            PD 23C16000            TC 5316200            or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 42 ex 8542 11 59	*21 *61	<p>UV erasable or non-erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 256 Kbits and an access time of less than 80 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 32 connections, with or without a quartz window on the upper surface and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters:            CY 7C 271            CY 7C 274            CY 7C 277            CY 7C 279            or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 44	*06	<p>Flash electrically erasable, programmable, read only memory (Flash-E<sup>2</sup>PROM), with a storage capacity of 1 Mbit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 53 mm, with not more than 48 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters:            28 F 010            28 F 210            48 F 010            or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 44 ex 8542 11 59	*07 *68	<p>UV erasable or non-erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 512 Kbits and an access time not exceeding 75 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 43 mm, with not more than 32 connections, with or without a quartz window on the upper surface and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters:            CY 7C 285-65            CY 7C 286            CY 7C 289-65            CY 7C 285-75            CY 7C 287            CY 7C 289-75            or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 46	*01	<p>Flash electrically erasable, programmable, read only memory (Flash-E<sup>2</sup>PROM) with a storage capacity of 2 Mbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 42 mm, with not more than 32 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: 28 F 020</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 46	*02	<p>Flash electrically erasable, programmable, read only memory (Flash-E<sup>2</sup>PROM) with a storage capacity of 4 Mbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 21 × 42 mm, with not more than 56 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: 28 F 040</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 48 ex 8542 11 59	*01 *66	<p>UV erasable or non-erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 8 Mbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 56 mm, with not more than 42 connections, with or without a quartz window on the upper surface and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: TC 578200</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 48 ex 8542 11 59	*02 *67	<p>UV erasable or non-erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 16 Mbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 56 mm, with not more than 42 connections, with or without a quartz window on the upper surface and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: TC 5716200</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 50	*02	<p>Electrically erasable, programmable, read only memory (E<sup>2</sup>PROM), with a storage capacity of 512 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 42 mm, with not more than 32 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: X 28C512</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 59	*26	<p>FIFO (first in, first out) read/write memory of C-MOS technology, for simultaneous reading and writing in one clocksignal, with a storage capacity of <math>512 \times 9</math> bits and an access time not exceeding 40 ns, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed <math>12 \times 43</math> mm, with not more than 32 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: CY7C441                      CY7C451</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 59	*27	<p>FIFO (first in, first out) read/write memory of C-MOS technology, for simultaneous reading and writing in one clocksignal or bidirectional, with a storage capacity of 18 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed <math>13 \times 43</math> mm, with not more than 32 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: CY7C439                      CY7C443                      CY7C453</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 62 ex 8542 11 64 ex 8542 11 67	*01 *01 *01	<p>Coprocessor of C-MOS technology, for multiple interfacing a parallel system bus to 8, 16 or 32 bits central processing units (CPUs), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed <math>41 \times 41</math> mm, with not more than 149 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures or figures and letters: 82389                              82C389</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 64	*40	<p>Microprocessor of C-MOS technology, with a processing capacity of 16 bits, consisting of a central processing unit (CPU) and an 8-bit or 16-bit external data-bus, contained in a housing the exterior dimensions of which do not exceed <math>36 \times 54</math> mm, with not more than 132 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: 80 C 186                      80 C 296                      DSP 56156                      Z 280 80 C 188                      ADSP 2100                      DSP 56200                      Z 70108 80 C 196                      DSP 56116                      XSP 56200                      Z 70116</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 64	*43	<p>Microprocessor of C-MOS technology, with a processing capacity of 16 bits, comprising a 4-channel direct memory access (DMA) controller, a universal asynchronous receiver/transmitter (UART), three 16-bit timer/counters, an 8-channel interrupt-controller, a memory-refresh controller, a clock-generator and a bus-controller with wait-state generator, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed <math>28 \times 29</math> mm, with not more than 120 connections and bearing:</p>	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 64 (cont'd)		<ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: PD 70236</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 64	*44	<p>Microprocessor of C-MOS technology, with a processing capacity of 16 bits, comprising 2 timers, a dynamic random-access memory (D-RAM) controller, a flash electrically erasable, programmable, read only memory (Flash E<sup>2</sup>PROM) controller, an electrically erasable, programmable, read only memory (E<sup>2</sup>PROM) controller, an universal asynchronous receiver/transmitter (UART), an address decoder circuit and registers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 160 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: 1TX9-0301</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 67	*39	<p>Microprocessor of C-MOS technology, with a processing capacity of 32 bits, comprising a 32-bit external data-bus and a 26-bit external address-bus, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 160 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: 86C020</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 67	*40	<p>Microprocessor of C-MOS technology, with a processing capacity of 32 bits, comprising a static random-access cache memory (S-Cache-RAM) with a storage capacity of 64 Kbits for instruction and data stream and a static random-access cache memory (S-Cache-RAM) with a storage capacity of 16 Kbits for instruction stream only, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 62 × 62 mm, with not more than 431 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: DC 262 A</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 71	*07	<p>Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 4 bits, providing driver-functions for liquid crystal displays (LCDs), comprising a read only memory, non-programmable (ROM) with a storage capacity of 108 or 216 Kbits, a random-access memory (RAM) with a storage capacity of 7196 or 16 412 bits, and 3 oscillators, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 22 × 22 mm, with not more than 100 connections and bearing:</p>	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 71 (cont'd)		<ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: TMC-17</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 71	*34	<p>Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a random-access memory (RAM) with a storage capacity of 2 or 8 Kbits, an electrically erasable, programmable, read only memory (E<sup>2</sup>PROM) with a storage capacity of 4 Kbits and an 8-channel analogue-to-digital converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 25 × 25 mm, with not more than 68 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: MC 68HC11F            SC 415111FU            SC 805666FN</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 71	*35	<p>Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 32 Kbits, a random-access memory (RAM) with a storage capacity of 1 Kbit, an 8-channel analogue-to-digital converter, two 16-bit timers each with two 16-bit registers and a serial synchronous communications interface consisting of an 8-bit serial shift register with serial data input, serial data output and serial shift clock, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 53 mm, with not more than 44 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: COP 884CF            COP 888CF</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 71	*36	<p>Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, consisting of a read only memory, non-programmable (ROM) with a storage capacity of 32 Kbits, a random-access memory (RAM) with a storage capacity of 1,5 Kbits, a full duplex universal asynchronous receiver/transmitter (UART), 2 analogue comparators, three 16-bit timers each with two 16-bit registers, and a serial synchronous communications interface consisting of an 8-bit serial shift register with serial data input, serial data output and serial shift clock, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 53 mm, with not more than 44 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: COP 888CG</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 71	*37	<p>Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, consisting of a read only memory, non-programmable (ROM) with a storage capacity of 64 Kbits, a random-access memory (RAM) with a storage capacity of 2 Kbits, a full duplex universal asynchronous receiver/transmitter (UART), 2 analogue comparators, three 16-bit timers each with two 16-bit registers, and a serial synchronous communications interface consisting of an 8-bit serial shift register with serial data input, serial data output and serial shift clock, in the form of a monolithic intergrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 53 mm, with not more than 44 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: COP 888EG</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 71	*38	<p>Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, consisting of a read only memory, non-programmable (ROM) with a storage capacity of 32 Kbits, a random-access memory (RAM) with a storage capacity of 1 Kbit, a 16-bit timer with a 16-bit register, and a serial synchronous communication interface consisting of an 8-bit serial shift register with serial data input, serial data output and serial shift clock, and in the form of a monolithic intergrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 53 mm, with not more than 44 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: COP 880C                      COP 881C</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 71	*39	<p>Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising sixteen 8-bit or eight 16-bit registers, a read only memory, non-programmable (ROM) or a programmable, read only memory (PROM), with a storage capacity of 128 Kbits, a random-access memory (RAM) with a storage capacity of 4 Kbits, 3 timers, a serial communications interface, an 8-channel analogue-to-digital converter and 9 input/output ports, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31 × 31 mm, with not more than 84 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: HD 6473308CP</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 71	*40	<p>Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, providing communication and control functions in local operating networks (LON), comprising three 8-bit central processing units (CPUs), a static random-access memory (S-RAM) with a storage capacity of 16 Kbits and an electrically erasable, programmable, read only memory (E<sup>2</sup>PROM) with a storage capacity of 4 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 15 mm, with not more than 64 connections and bearing:</p>	



CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 71 (cont'd)		<ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: MC 143150 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 71	*41	<p>Microcontroller or microcomputer of C-MOS-technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 8 or 16 Kbits, a random-access memory (RAM) with a storage capacity of 512 bits or 1 Kbit and a synchronous serial communications interface circuit consisting of an 8-bit serial shift register with a serial data input, a serial data output and a serial shift clock, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 38 mm, with not more than 28 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: COP 820                      COP 840 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 73	*05	<p>Microcontroller or microcomputer with a processing capacity of 16 bits, consisting of a read only memory, non-programmable (ROM) or a programmable, non-erasable, read only memory (PROM) or a UV erasable, programmable, read only memory (EPROM) with a storage capacity not exceeding 128 Kbits, a random-access memory (RAM) with a storage capacity not exceeding 6 Kbits, an analogue-to-digital converter with sample/hold, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 40 × 40 mm, with not more than 84 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures or figures and letters: 8396              8397              8796              83C19              83C198              87C196 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 73	*07	<p>Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, comprising a multiplier/accumulator (MAC), an arithmetic-logic shifter, a microprocessor interface port, a read only memory (ROM) with a storage capacity of 48 Kbits, a static random-access memory (S-RAM) with a storage capacity of 16 Kbits, an analogue-to-digital converter, a digital-to-analogue converter and a programmable timer, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 40 × 40 mm, with not more than 144 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: 21msp52BS-52 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 73	*08	<p>Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, comprising a read only memory, non-programmable (ROM) or a programmable, non-erasable, read only memory (PROM) or a UV erasable, programmable, read only memory (EPROM) with a storage capacity of 128 or 256 Kbits, a random-access memory (RAM) with a storage capacity of 4 or 16 Kbits and an 8-bit analogue-to-digital converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18×58 mm, with not more than 80 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: M 37702 E2            M 37702 E4            M 37702 M2            M 37702 M4 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 74	*13	<p>Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 32 bits, having the function of audio-data processing, comprising an arithmetic-logic unit (ALU) of 32 bits, a multiplier/accumulator (MAC) of 52 bits, 2 dynamic random-access memories (D-RAMs) with a total storage capacity of 12 Kbits and 2 static random-access memories (S-RAMs) with a total storage capacity of 14 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 23×82 mm, with not more than 64 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: TMC 57000            TMC 57001 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 79	*01	<p>Digital signal synthesizer based on standard cells of C-MOS technology, comprising 32 independent programmable channels, a clock generator, an input/output decoder, a microprocessor with a processing capacity of 8 bits, 2 timers, an interrupt controller, 2 digital-to-analogue converters and an analogue-to-digital converter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31×31 mm, with not more than 128 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: VY 06243 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 79	*02	<p>Interface and control circuit based on standard cells of C-MOS technology, capable of interfacing between a 32-bit data-bus and a 16-port asynchronous multiplexer, comprising data-registers, control- and status-registers, and a FIFO (first in, first out) read/write memory controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29×29 mm, with not more than 160 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: 1TWA-0302 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0

CN code	TARIC	Description	Rate of autonomous duty (%)																																																																											
ex 8542 11 79	*03	<p>Audio digital filter based on standard cells of C-MOS technology, with 16 channels, each of them real time programmable with 20 parameters or more, comprising a multiplier/accumulator (MAC), a timer and 2 random-access memories (RAMs) for the storage of parameters and of temporary processing data, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 25 × 25 mm, with not more than 68 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: VC 5396</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0																																																																											
ex 8542 11 80	*06	<p>Programmable logic device, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 85 × 124 mm, with or without a quartz window on the upper surface, with not more than 300 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: <table border="0" data-bbox="430 950 1197 1354"> <tr> <td>16 L8-W</td> <td>C 16 R 8</td> <td>CY7C361</td> <td>EPM 7128</td> <td>XC 3030</td> </tr> <tr> <td>16 P 8</td> <td>C 20 G 10</td> <td>EP 600</td> <td>EPM 7160</td> <td>XC 3042</td> </tr> <tr> <td>16 R4-W</td> <td>C 22 V 10</td> <td>EP 610</td> <td>EPM 7192</td> <td>XC 3064</td> </tr> <tr> <td>16 R6-W</td> <td>CY7B326</td> <td>EP 630</td> <td>EPM 7256</td> <td>XC 3090</td> </tr> <tr> <td>16 R8-W</td> <td>CY7B336</td> <td>EP 910</td> <td>EPM 7320</td> <td>XC 4002</td> </tr> <tr> <td>16 RP 4</td> <td>CY7B337</td> <td>EP 1800</td> <td>EPM 7384</td> <td>XC 4003</td> </tr> <tr> <td>22 V10-W</td> <td>CY7B338</td> <td>EP 1810</td> <td>EPM 7512</td> <td>XC 4004</td> </tr> <tr> <td>A 1010</td> <td>CY7B339</td> <td>EP 1830</td> <td>EPM 7768</td> <td>XC 4005</td> </tr> <tr> <td>A 1020</td> <td>CY7C330</td> <td>EPM 5032</td> <td>EPM 71024</td> <td>XC 4006</td> </tr> <tr> <td>A 1225</td> <td>CY7C331</td> <td>EPM 5064</td> <td>TPC 1225</td> <td>XC 4008</td> </tr> <tr> <td>A 1240</td> <td>CY7C332</td> <td>EPM 5128</td> <td>TPC 1240</td> <td>XC 4010</td> </tr> <tr> <td>A 1280</td> <td>CY7C342</td> <td>EPM 5130</td> <td>TPC 1280</td> <td>XC 4013</td> </tr> <tr> <td>C 16 L 8</td> <td>CY7C343</td> <td>EPM 5192</td> <td>XC 2018</td> <td>XC 4016</td> </tr> <tr> <td>C 16 R 4</td> <td>CY7C344</td> <td>EPM 7032</td> <td>XC 2064</td> <td>XC 4020</td> </tr> <tr> <td>C 16 R 6</td> <td>CY7C345</td> <td>EPM 7096</td> <td>XC 3020</td> <td></td> </tr> </table> </li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	16 L8-W	C 16 R 8	CY7C361	EPM 7128	XC 3030	16 P 8	C 20 G 10	EP 600	EPM 7160	XC 3042	16 R4-W	C 22 V 10	EP 610	EPM 7192	XC 3064	16 R6-W	CY7B326	EP 630	EPM 7256	XC 3090	16 R8-W	CY7B336	EP 910	EPM 7320	XC 4002	16 RP 4	CY7B337	EP 1800	EPM 7384	XC 4003	22 V10-W	CY7B338	EP 1810	EPM 7512	XC 4004	A 1010	CY7B339	EP 1830	EPM 7768	XC 4005	A 1020	CY7C330	EPM 5032	EPM 71024	XC 4006	A 1225	CY7C331	EPM 5064	TPC 1225	XC 4008	A 1240	CY7C332	EPM 5128	TPC 1240	XC 4010	A 1280	CY7C342	EPM 5130	TPC 1280	XC 4013	C 16 L 8	CY7C343	EPM 5192	XC 2018	XC 4016	C 16 R 4	CY7C344	EPM 7032	XC 2064	XC 4020	C 16 R 6	CY7C345	EPM 7096	XC 3020		0
16 L8-W	C 16 R 8	CY7C361	EPM 7128	XC 3030																																																																										
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A 1020	CY7C330	EPM 5032	EPM 71024	XC 4006																																																																										
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A 1240	CY7C332	EPM 5128	TPC 1240	XC 4010																																																																										
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C 16 R 6	CY7C345	EPM 7096	XC 3020																																																																											
ex 8542 11 82	*06	<p>Bus controller of C-MOS technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 41 × 41 mm, with not more than 208 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures or figures and letters: <table border="0" data-bbox="430 1681 1181 1823"> <tr> <td>82308</td> <td>82 C 211</td> <td>GC 181</td> <td>VL 82 C 331</td> </tr> <tr> <td>82309</td> <td>82 C 288</td> <td>L1A 4601</td> <td>VY 86 C 410</td> </tr> <tr> <td>82355</td> <td>82 C 301</td> <td>MSM 6307</td> <td></td> </tr> <tr> <td>82358</td> <td>82 C 362</td> <td>TACT 83443</td> <td></td> </tr> <tr> <td>82 C 88</td> <td>CA 91C014</td> <td>VIC 068</td> <td></td> </tr> </table> </li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	82308	82 C 211	GC 181	VL 82 C 331	82309	82 C 288	L1A 4601	VY 86 C 410	82355	82 C 301	MSM 6307		82358	82 C 362	TACT 83443		82 C 88	CA 91C014	VIC 068		0																																																							
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82358	82 C 362	TACT 83443																																																																												
82 C 88	CA 91C014	VIC 068																																																																												
ex 8542 11 82	*07	<p>Control circuit of C-MOS technology, for system buses and peripheral buses and for the generation of clock signals (System Controller), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 33 × 33 mm, with not more than 160 connections and bearing:</p>																																																																												

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 82 (cont'd)		<p>— an identification marking consisting of or including one of the following combinations of figures and letters: 82C101                      82C320                      82C461                      82C493</p> <p>or</p> <p>— other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 11 82	*49	<p>Dynamic random-access read/write memory controller of C-MOS technology, capable of addressing memory by page (page mode operation) and simultaneous processing (interleaving) of separate memories, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 33 × 33 mm, with not more than 160 connections and bearing:</p> <p>— an identification marking consisting of or including one of the following combinations of figures and letters: 82 C 283                      GC 113                      HT 113</p> <p>or</p> <p>— other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 11 82	*51	<p>Control circuit of C-MOS technology, capable of managing battery-charge and consumption of batteries, comprising a random-access memory (RAM) with a storage capacity of 256 bits, a charge controller, a discharge controller, a battery monitor and a gas-gauge control register, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 33 mm, with not more than 24 connections and bearing:</p> <p>— an identification marking consisting of or including one of the following combinations of figures and letters: BQ 2001                      BQ 2002</p> <p>or</p> <p>— other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 11 82	*52	<p>Control circuit of C-MOS technology, capable of processing read-signals and of controlling the motor of a compact-disc player, comprising a central processing unit (CPU) interface, an error detection/correction circuit, a read-signal demodulator, a phase locked loop (PLL) circuit and a constant-linear-velocity (CLV) controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 21 mm, with not more than 80 connections and bearing:</p> <p>— an identification marking consisting of or including one of the following combinations of figures and letters: CXD 1125                      CXD 1130                      CXD 1135                      CXD 1167</p> <p>or</p> <p>— other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 11 82	*53	<p>Control circuit of C-MOS technology, for microcontroller or microcomputer systems with a processing capacity of 16 or 32 bits, providing programmable-memory-configuration control, programmable wait-states and programmable command-delays for memory- and bus-timing, comprising a real-time clock, a static random-access memory (S-RAM) with a storage capacity of 1 Kbit, an interrupt-controller, a direct memory access (DMA) controller, a programmable DMA-clock, and timers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 208 connections and bearing:</p>	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 82 (cont'd)		<ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: TACT 82S411 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 82	*54	<p>Control circuit of C-MOS technology, capable of controlling signals of a microprocessor with a processing capacity of 32 bits and access to a dynamic random-access memory (D-RAM) and a cache memory, 8-bit, 16-bit or 32-bit system-buses and providing bus-arbitration, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31×31 mm, with not more than 160 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: 82C311 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 82	*55	<p>Asynchronous serial communication controller of C-MOS technology, having 8 independent channels capable of full duplex data-transfer at a rate of 38 400 bits per second, comprising a FIFO (first in, first out) read/write memory with a storage capacity of 192 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31×31 mm, with not more than 84 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: CL-CD180 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 82	*67	<p>Hard-disc controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 31×53 mm, with not more than 144 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures or figures and letters: 1454-001    HDC 9224    PD 7261    WD 2010    WD 42C22 8980        HDC 9234    PD 7262    WD 5010    Z 86 C 99 ADS 10C00   OTI 018    WD 1010    WD 5011 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 82	*79	<p>Serial communication controller, with 2 independent channels capable of full duplex data-transfer at a maximum rate of 1,6 Mbits/sec or more but not exceeding 4 Mbits/sec, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 21×62 mm, with not more than 52 connections and bearing:</p>	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 82 (cont'd)		<ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: PD 72001      SCN 68562      Z 80 C 30      Z 85 C 30      Z 85 C 35</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 82	*84	<p>Control circuit for liquid crystal displays (LCDs) of C-MOS technology, with 40 or more output channels, and having a drive voltage of more than 10 V, in the form of a monolithic integrated circuit, with not more than 165 connections as connections on a plastic support and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: HD 61104T      HD 61105T      HD 66106T      HD 66107T</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 82	*85	<p>Control circuit of N-MOS (including H-MOS) technology, for DC motors, comprising a digital 16-bit filter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 14×37 mm, with not more than 28 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: LM 629</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 82	*86	<p>Control circuit of C-MOS technology, for a fibre-optic local area network (LAN), providing media access control, capable of receiving, transmitting, stripping and repeating data, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 28×28 mm, with not more than 132 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters : DP 83261</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 82	*87	<p>Serial and parallel communication controller of C-MOS technology, comprising 2 universal asynchronous receiver/transmitters (UARTs), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×17 mm, with not more than 84 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: WD 76C30</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 82	*88	<p>Cathode-ray tube video controller, in the form of a monolithic integrated circuit contained in a housing the exterior dimensions of which do not exceed 36 × 62 mm, with not more than 184 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: 86 C 911                      WD 90 C 30</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 84*	*01	<p>Bus interface circuit of C-MOS technology, for interfacing a 8- or 16-bit address/data peripheral bus or memory bus to a 32-bit address/data-bus of a central processing unit (CPU), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 28 × 28 mm, with not more than 160 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 82351                              GC 133</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 84	*07	<p>Interface circuit of C-MOS technology, capable of dual-port interfacing 16-bit data between a system data-bus, a memory data-bus and a microprocessor data-bus, comprising an internal data-bus, latches, buffers, multiplexers and parity-check circuits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 34 × 34 mm, with not more than 164 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures: 82353</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 84	*22	<p>Enhanced programmable communications interface (EPCI), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 39 mm, with not more than 28 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures: 2661                                      68661</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 84	*30	<p>Bus controller and interface of C-MOS technology, comprising one or two direct memory access (DMA) controllers, one or two interrupt controllers and a timer/counter, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 33 × 33 mm, with not more than 160 connections and bearing:</p>	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 84 (cont'd)		<p>— an identification marking consisting of or including one of the following combinations of figures and letters: 82C100                      82C300                      82C836                      WD 76 C 10 or</p> <p>— other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 11 84	*47	<p>Interface and control circuit of C-MOS technology, comprising 2 universal asynchronous receiver/transmitters (UARTs), a parallel-data port, a hard-disc interface and a floppy-disc controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 21 mm, with not more than 100 connections and bearing:</p> <p>— an identification marking consisting of or including the following combination of figures and letters: 82C711 or</p> <p>— other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 11 84	*48	<p>Interface and control circuit of C-MOS technology, programmable for interfacing signals between video-graphic-array (VGA) controllers and cathode-ray tube (CRT) displays, liquid crystal displays (LCDs), light-emitting-diode (LED) displays or plasma-displays, capable of simultaneously controlling a CRT-display and a LCD display, comprising a digital-to-analogue video-converter with random-access memory (RAMDAC), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 21 mm, with not more than 100 connections and bearing:</p> <p>— an identification marking consisting of or including the following combination of figures and letters: CL-GD6340 or</p> <p>— other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 11 84	*49	<p>Repeater interface and control circuit of C-MOS technology, comprising 7 or 12 transmission/reception interface ports, an attachment-unit interface (AUI) port, a phase locked loop (PLL) decoder, a 32-bit buffer and a bus-interface for communication between repeater interface controllers (RICs), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 45 × 45 mm, with not more than 160 connections and bearing:</p> <p>— an identification marking consisting of or including one of the following combinations of figures and letters: DP 83950                      DP 83955 or</p> <p>— other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 11 84	*50	<p>Interface circuit of C-MOS technology, for interfacing data between a 32-bit bus and input/output-registers and/or interface circuits, providing direct memory access (DMA) control, comprising a FIFO (first in, first out) read/write memory with a storage capacity of 256 bits, a data-latch, an address-latch and 32-bit registers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 37 × 37 mm, with not more than 132 connections and bearing:</p>	



CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 84 (cont'd)		<ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: 1XB9-0401 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 84	*51	<p>Bus interface and control circuit of C-MOS technology, comprising 2 programmable interrupt-controllers, 2 timers, 3 counters, a real-time clock, a random-access memory (RAM) with a storage capacity of 912 bits, a memory-interface circuit, a floppy-disc controller interface circuit, a hard-disc controller interface circuit, a numeric-coprocessor interface and a keyboard-interface, capable of address-enable generation, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 160 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: TACT 84544 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 84	*52	<p>Interface and control circuit of C-MOS technology, for read/write data between a digital-audio-tape storage unit and a microprocessor, comprising a buffer-manager circuit for a dynamic random-access memory (D-RAM), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 208 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: 1XK2-0301 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 84	*85	<p>Interface circuit of BiMOS technology, for a fibre-optic local area network (LAN), capable of interfacing data streams between a media access controller, a clock recovery circuit, a control bus and a fibre-optic transmitter, comprising a transmitter, a receiver, a configuration switch and registers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 132 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: DP 83251                      DP 83255 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 84	*86	<p>Interface circuit of C-MOS technology, for a fibre-optic local area network (LAN), capable of interfacing between a media access controller and a central processing unit (CPU), comprising FiFo (first in, first out) read/write memories and a bus interface unit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 28 × 28 mm, with not more than 160 connections and bearing:</p>	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 84 (cont'd)		<ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: DP 83265 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 84	*87	<p>Control and interface circuit of C-MOS technology, for a serial local area network (LAN), comprising a twisted pair cable interface, an encoder/decoder using Manchester code (MED), a media-access controller, a direct memory access (DMA) controller and a FIFO (first in, first out) read/write memory with a storage capacity of 128 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 100 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: DP 83902 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 86	*11	<p>Data-buffer circuit of C-MOS technology, for data transfer and data conversion between a central processing unit (CPU) data-bus, a memory data-bus and 2 peripheral data-busses, comprising a parity-generator, a parity-error-detection circuit and 7 multiplexers, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 29 × 29 mm, with not more than 128 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: VL 82C332 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 86	*13	<p>16-, 18- or 20-bit buffer and line driver of C-MOS technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 20 mm, with not more than 56 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: 74 AC 16240      74 AC 16825      74 AC 16827      74 ACT 16244 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 86	*14	<p>16- or 20-bit flip-flop of the D-type of C-MOS technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 20 mm, with not more than 56 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: 74 AC 16821      74 ACT 16374 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86	*15	<p>Dual-tone multi-frequency (DTMF) receiver of C-MOS technology, capable of decoding DTMF signals to 4-bit binary data, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 30 mm, with not more than 22 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: M-957 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 86	*16	<p>Transmitter/receiver of C-MOS technology, for signals between an encoder/decoder using Manchester code (MED) or an interface unit and a twisted pair cable or a coaxial cable in a local area network (LAN), comprising an oscillator and a collision detector circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 20 × 40 mm, with not more than 28 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: 83C94                      83C92                      Am 79C98 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 86	*17	<p>Clock recovery circuit of BiMOS technology, for a fibre-optic local area network (LAN), capable of extracting a 125 MHz clock from the incoming bit stream, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 12 mm, with not more than 28 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: DP 83231 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 86	*18	<p>Clock circuit of BiMOS technology, for a fibre-optic local area network (LAN), capable of generating and distributing emitter-coupled logic (ECL) clock-signals of 12,5 and 125 MHz and transistor-transistor logic (TTL) clock-signals of 12,5 and 25 MHz, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 12 mm, with not more than 28 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: DP 83241 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 86	*19	<p>Universal synchronous receiver/transmitter of C-MOS technology (C-MOS USRT), capable of full duplex digital data transfer with a speed of 144 Kbits/s over a distance of 1,8 km, comprising a modulator, a demodulator, a transmit buffer, a receiver buffer, a control register and a status register, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13 × 26 mm, with not more than 28 connections and bearing:</p>	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86 (cont'd)		<p>— an identification marking consisting of or including one of the following combinations of figures and letters:  TP 3401                      TP 3402                      TP 3403  or  — other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 11 86	*20	<p>Quadruple encoder/decoder with pulse-code-modulation filters of C-MOS technology, comprising amplifiers for sidetone balance, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 18 mm, with not more than 44 connections and bearing:  — an identification marking consisting of or including the following combination of figures and letters:  QMV 365  or  — other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 11 86	*28	<p>Triple 8-bit digital-to-analogue video converter with random-access memory (RAMDAC) of C-MOS technology, having 3 pixel ports each associated with a colour palette register with a storage capacity of 256 × 8 bits and 1 pixel port associated with 3 overlay registers each with a storage capacity of 15 × 8 bits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 26 × 26 mm, with not more than 68 connections and bearing:  — an identification marking consisting of or including the following combination of figures and letters:  Bt473  or  — other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 11 86	*29	<p>Dual 7-bit digital-to-analogue converter of C-MOS technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 29 mm, with not more than 28 connections and bearing:  — an identification marking consisting of or including the following combination of figures and letters:  PBM 3960  or  — other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 11 86	*30	<p>Decoder of C-MOS technology, for demodulating and demultiplexing of stereo signals, comprising an interface circuit of a digital-to-analogue converter having a output clock signal of 8,192 or 16,384 MHz, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 16 × 63 mm, with not more than 48 connections and bearing:  — an identification marking consisting of or including the following combination of figures and letters:  CF 70088  or  — other identification markings relating to devices complying with the abovementioned description</p>	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86	*31	<p>Synchronizing circuit combined with a scan and signal distributor of C-MOS technology, comprising a control unit, a contact bounce elimination circuit, a 17-bit shift register and a data output formatting unit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 54 × 54 mm, with not more than 68 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: QMV 222</li> <li>or</li> <li>— other identification marking relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 86	*53	<p>Programmable clock-generator of BiMOS technology, capable of generating frequencies between 0,78125 MHz and 160 MHz, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 13 × 13 mm, with not more than 28 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: DP 8531                      DP 8532</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 86	*60	<p>Image compression/decompression circuit of C-MOS technology, comprising a video bus interface, a pixel data stream compression/decompression unit and a microprocessor bus interface, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 45 × 45 mm, with not more than 144 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: CL 550</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 86	*68	<p>Modulator/demodulator of C-MOS technology (C-MOS-Modem), for full duplex data-transfer via a telephone line at a rate of 2 400 bits per second and for half duplex transfer of image telegraphy (facsimile) at a rate of 4 800 or 9 600 bits per second, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 14 × 37 mm, with not more than 28 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: SC 11046                      SC 11054</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 86	*77	<p>Dual universal asynchronous receiver/transmitter (DUART), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 53 mm, with not more than 100 connections and bearing:</p>	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 86 (cont'd)		<ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures or figures and letters: 2681                      PC 87310</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 86	*80	<p>Comparator circuit of C-MOS technology, capable of comparing cache-address tags, comprising a static random-access memory (S-RAM) with a storage capacity of 16 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 33 mm, with not more than 24 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: MCM 62350</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 86	*81	<p>Digital filter of C-MOS technology, capable of quadruple or octuple over-sampling for 2 channels, comprising a read only memory, non-programmable (ROM) and a random-access memory (RAM), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 14 × 36 mm, with not more than 40 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: TMS 57021</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 86	*85	<p>Transmitter/receiver of C-MOS technology, operating with a supply current not exceeding 3 mA, comprising 2 driver circuits, 3 receiver circuits and a thermal protection circuit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 9 × 12 mm, with not more than 18 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: LTC 902</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 90	*75	<p>Static random-access memory of GaAs technology (GaAs S-RAM), with a storage capacity of 1 Kbit, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 17 mm, with not more than 44 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: 12G014</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 90	*76	<p>Programmable, non-erasable, read only memory (PROM) of GaAs technology with a storage capacity of 4 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×17 mm, with not more than 44 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: 14GD048                      14GM048</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 90	*77	<p>Static random-access memory of GaAs technology (GaAs S-RAM), with a storage capacity of 4 Kbits, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×17 mm, with not more than 44 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: 12G044</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 95	*18	<p>Control circuit for disk storage units based on gate arrays of bipolar technology, comprising 1 800 gates, with not more than 80 emitter-coupled logic (ECL) and/or transistor-transistor logic (TTL) inputs/outputs, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30×30 mm, with not more than 68 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: AmMPA 1850</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 95	*38	<p>2-, 4-, 6-, 10- or 14-channel controller of bipolar technology, for controlling read/write signals from magnetic heads in hard-disc units, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 19×19 mm, with not more than 44 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters : 32 R 2020 R                      32 R 510 A                      32 R 522 32 R 2021 R                      32 R 5121</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 95	*62	<p>Driver circuit of GaAs technology, for controlling laser diodes or other light-emitting diodes (LEDs), in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×17 mm, with not more than 44 connections and bearing:</p>	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 95 (cont'd)		<ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: 16G075                      16G076</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 95	*63	<p>Duodecimal translator of bipolar technology, to convert to emitter-coupled logic (ECL) levels or to transistor-transistor logic (TTL) levels, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 18 × 18 mm, with not more than 124 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 57-240245                      MB 768</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 98	*04	<p>Monolithic integrated circuit (read/write data processor circuit) for the amplification and conversion of read signals and conversion of write signals for hard-disc drives, contained in a housing the exterior dimensions of which do not exceed 19 × 38 mm, with not more than 28 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of letters and figures: 32 P 540                      32 P 541</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 98	*19	<p>9-bit analogue-to-digital converter of bipolar technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 30 × 30 mm, with not more than 68 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: TDC 1049</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 11 98	*20	<p>Clock and data recovery circuit of GaAs technology, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 17 mm, with not more than 44 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: 16G040</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0



CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 98	*21	<p>Comparator circuit of GaAs technology, for phase and frequency differences of frequencies not exceeding 1 GHz, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 17×17 mm, with not more than 44 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: 16G044 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 30	*05	<p>Amplifier of bipolar technology, with a supply current not exceeding 1,5 mA, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 9×9 mm, with not more than 20 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: LM 1964 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 30	*06	<p>Amplifier of bipolar technology, with a typical operating frequency of 1,3 GHz, 2,3 GHz or 3 GHz and a single supply voltage of 5 V, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 3×3 mm, with not more than 6 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: C1D      C1E      C1F      C1G      C1H      C1J or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 30	*07	<p>Amplifier with an offset voltage not exceeding 525 μV at 25 °C, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 5×7 mm, with not more than 8 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures: 1006 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 30	*08	<p>Amplifier of GaAs technology, having a nominal gain of 18 or more but not exceeding 30 dB and a frequency range of not more than 1,9 GHz, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 17×17 mm, with not more than 44 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: 16G071                      16G072                      16G074 or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 30	*30	<p>Dual or quadruple amplifier operating with a supply current per amplifier not exceeding 30 <math>\mu</math>A, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 <math>\times</math> 11 mm, with not more than 16 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters:            LT 1178                    LT 1179</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 50	*20	<p>Voltage regulator with an input voltage range of 3 V or more but not exceeding 60 V and a quiescent current of 6 mA, comprising an internal 1,25 A, 2,5 A or 5 A switch circuit, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 11 <math>\times</math> 39 mm, with not more than 8 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters:            LT 1070            LT 1071            LT 1170            LT 1171            LT 1172</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 50	*30	<p>Adjustable shunt voltage regulator, comprising an internal voltage reference and divider resistors with a collector (sink) current of 1 mA or more but not exceeding 100 mA and an initial voltage reference tolerance of 0,4 %, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 <math>\times</math> 11 mm, with not more than 8 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters:            LT 1431</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 50	*40	<p>Variable voltage regulator with a supply current not exceeding 120 <math>\mu</math>A at an output current not exceeding 100 <math>\mu</math>A and a dropout voltage not exceeding 0,85 V at an output current of 125 mA, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 <math>\times</math> 20 mm, with not more than 16 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters:            LT 1020                    LT 1120</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 50	*50	<p>Voltage regulator with an output voltage of 2,1 V (<math>\pm</math>2,5 %) or 3 V (<math>\pm</math>2,5 %) at a nominal output current of 40 mA, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 6 <math>\times</math> 6 mm, with not more than 3 connections and bearing:</p>	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 50 (cont'd)		<ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: 0 C (RH5 RA 30 AA) 1 B (RH5 RA 21 AA)</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 60	*08	<p>Current-control circuit of bipolar technology, capable of driving a continuous output current of 2 A, having an output-error detection function, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 11 × 16 mm, with not more than 5 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: PBD 3548</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 60	*09	<p>Control circuit of BiMOS technology, capable of driving loads from the positive side of a power supply, providing a voltage-transient protection of -100 V or more but not exceeding +100 V, having an output leak-current not exceeding 150 µA, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 11 mm, with not more than 16 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: MC 3399DW</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 60	*10	<p>Circuit for driving current in a winding of linear motors or motors with rotating arms, of bipolar technology, working with an output voltage not exceeding 45 V at an output current not exceeding 1,8 A, in form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 23 mm, with not more than 28 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: PBL 3717                      PBL 3770</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 60	*11	<p>Circuit for driving linear motors or motors with rotating arms, of bipolar technology, working with an output voltage not exceeding 45 V at an output current not exceeding 1 A, comprising a clockgenerator, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 23 mm, with not more than 28 connections and bearing:</p>	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 60 (cont'd)		<p>— an identification marking consisting of or including one of the following combinations of figures and letters: PBL 3771                      PBL 3772 or — other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 19 70	*30	<p>Interface circuit of bipolar technology, for interfacing data and power between a microcontroller or microcomputer and a read/write terminal, comprising a bridge rectifier, a power supply regulator, a reset circuit, a transmitter and a receiver, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 11 mm, with not more than 16 connections and bearing:</p> <p>— an identification marking consisting of or including the following combination of figures and letters: LB 1167A or — other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 19 80	*20	<p>Interline charge-coupled image sensor, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 21 × 32 mm, with not more than 20 connections and bearing:</p> <p>— an identification marking consisting of or including one of the following combinations of figures and letters: ICX 018    ICX 021    ICX 022    ICX 024    ICX 038    ICX 039 or — other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 19 80	*25	<p>Half-bridgerectifier, consisting of 2 field effect transistors of MOS technology (MOSFETs), capable of driving inductive or capacitive loads with a nominal voltage of 50 V and a nominal current of 2 A, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 6 × 11 mm, with not more than 16 connections and bearing:</p> <p>— an identification marking consisting of or including the following combination of figures and letters: Si9950DY or — other identification markings relating to devices complying with the abovementioned description</p>	0
ex 8542 19 80	*26	<p>Voltage converter and regulator of bipolar technology, with a voltage loss not exceeding 1,6 V at an output current of 100 mA, operating with a supply voltage range of 3,5 V or more but not exceeding 15 V, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 11 mm, with not more than 16 connections and bearing:</p> <p>— an identification marking consisting of or including the following combination of figures and letters: LT 1054 or — other identification markings relating to devices complying with the abovementioned description</p>	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 80	*27	<p>5-channel voltage comparator for monitoring lamp-circuits, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 10 × 28 mm, with not more than 20 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: AD 22001</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 80	*28	<p>Image sensor consisting of 16 rows of not more than 992 photosensitive areas and a matrix linked to a shift register, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 12 × 28 mm, with not more than 22 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: RA 16 × 62</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 80	*32	<p>Speech-transfer circuit of bipolar technology, operating at a supply voltage of 2,3 V or more but not exceeding 22 V, providing continuously regulation of transmit and receive gain and a mute function, comprising 4 amplifiers, an internal voltage reference, 2 DC regulators and a power down function, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 13 mm, with not more than 20 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: PBL 3850</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 80	*34	<p>Passive decoder of BiMOS technology, comprising a fixed matrix, a 7-kHz filter, a noise-reducing circuit, a digital delay circuit and a memory-control circuit, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 58 mm, with not more than 64 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: LV 1000</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 80	*35	<p>Matrix decoder of BiMOS technology, comprising an adaptive matrix circuit, automatic-balance buffers, a noise generator and a control circuit, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 15 × 63 mm, with not more than 48 connections and bearing:</p>	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 80 (cont'd)		<ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: SSM 2125                      SSM 2126</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 80	*36	<p>Voice-switched speakerphone circuit of bipolar technology, capable of half duplex transmission and reception of speech, comprising attenuators, level-detectors, a dial-tone detector, an algorithm-control circuit and amplifiers, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 19 mm, with not more than 28 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: MC 34118</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 80	*37	<p>Serial 13-bit linear encoder/decoder of C-MOS technology, providing digital-to-analogue and analogue-to-digital conversion, comprising 2 sample and hold circuits, an comparator/ amplifier, a data-selector circuit, a successive approximation register, 2 shift registers, a sequence controller and a voltage reference circuit, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 20 mm, with not more than 16 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: MC 145402</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 80	*38	<p>Encoder/decoder with pulse-code-modulation filter of C-MOS technology, operating with a +5 V single-power supply, comprising an analogue-to-digital converter and a digital-to-analogue converter, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 13 mm, with not more than 20 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: MC 145480</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 80	*39	<p>Encoder/decoder with pulse-code-modulation filter of C-MOS technology, with a dual-power supply and having a typical dissipation rate of 50 mW, comprising an analogue-to-digital converter and a digital-to-analogue converter, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 11 mm, with not more than 16 connections and bearing:</p>	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 80 (cont'd)		<ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: MC 145503</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 80	*40	<p>Adaptive differentiated pulse-code-modulation circuit of C-MOS technology, for encoding/decoding data, capable of full duplex data-transmission between a channel with a transfer rate of 64 Kbits per second and a channel with a transfer rate of 16 Kbits, 24 Kbits, 32 Kbits or 64 Kbits per second, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 20 mm, with not more than 16 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: MC 145532</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 80	*41	<p>Voltage reference circuit providing a typical output voltage of 5, 7 or 10 V with a drift slope (output voltage temperature co-efficient) not exceeding 20 ppm/°C and an output voltage noise not exceeding 6 µVrms, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 8 × 11 mm, with not more than 8 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: LT 1021</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 80	*42	<p>Voltage reference circuit with a reverse breakdown of 1,235 V (± 4 mV) or 2,5 V (± 20 mV), in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 5 × 6 mm or the diameter of which does not exceed 6 mm, with not more than 8 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: LT 1004</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 80	*43	<p>Bidirectional switch circuit of C-MOS technology, capable of switching analogue signals of -5 V and of +5 V, comprising 4 field-effect transistors (FETs), in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 9 × 11 mm, with not more than 18 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: SD 5401 CY</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 80	*44	<p>Modulator of bipolar technology, operating in the UHF band, for the conversion of audio and video signals; in a frequency range of 470 MHz or more but not exceeding 630 MHz, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 6 × 11 mm, with not more than 16 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: ALP 101                      CXA 1333</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 80	*45	<p>Full-frame charge-coupled image sensor with single-phase clocking and not more than 1 048 576 photosensitive areas, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 32 × 32 mm, with not more than 160 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: TC 223</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 80	*46	<p>Encoder/decoder with pulse-code-modulation filters of C-MOS technology, capable of voice digitization and reconstruction at a speed of 64 Kbits/s or more but not exceeding 2 048 Kbits/s, with a single power supply of 5 V, a power dissipation not exceeding 37 mW in operating mode and not exceeding 3 mW in power down mode and capable of automatically entering in power down mode with clock stop, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 13 × 20 mm, with not more than 28 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: 7508 B                      7509 B</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 19 80	*47	<p>Programmable diode array, consisting of 14 individual diodes and a rectifier, of GaAs technology, in the form of a monolithic integrated analogue circuit, contained in a housing the exterior dimensions of which do not exceed 17 × 17 mm, with not more than 44 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: 16G010                      16G011</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 20 30	*50	<p>12-bit analogue-to-digital converter of C-MOS technology, comprising a sample and hold amplifier having a dynamic performance of 0,5 MHz per second or more, in the form of a hybrid integrated circuit, contained in a housing the exterior dimensions of which do not exceed 21 × 34 mm, with not more than 24 connections and bearing:</p>	



CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 20 30 (cont'd)		<ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: ADS 111                      ADS 112                      ADS 117</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 20 50	*40	<p>Amplifier with an input power of 1 mW and an output power of 3,5 W at a frequency range of 890 MHz or more but not exceeding 915 MHz, in the form of a hybrid integrated circuit, contained in a housing the exterior dimensions of which do not exceed 5 × 15 × 34 mm, with not more than 6 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: XHW 903</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 20 50	*50	<p>Amplifier, operating with a supply voltage of 28 V, for frequencies of 1 625 MHz or more but not exceeding 1 645 MHz, in the form of a hybrid integrated circuit, contained in a housing the exterior dimensions of which do not exceed 27 × 96 mm, with not more than 6 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures and letters: STM 1645-30</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8542 80 00	*01	<p>Dual field-effect transistor of N-MOS (including H-MOS) technology (Dual N/H-MOSFET), having a drain-to-source breakdown-voltage of 20 V or more, operating with a drain-current not exceeding 3,5 A and with a dissipation rate not exceeding 2 W, contained in a housing the exterior dimensions of which do not exceed 6 × 7 mm, with not more than 8 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including the following combination of figures: 9956</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0
ex 8543 80 80	*93	<p>Opto-electronic circuit consisting of one or more light-emitting diodes (LEDs) and one photodiode with amplifier circuit and an integrated logic gate arrays circuit or one or more light-emitting diodes (LEDs) and of several photodiodes with amplifier circuit, contained in a plastic housing with not more than 8 connections and bearing:</p> <ul style="list-style-type: none"> <li>— an identification marking consisting of or including one of the following combinations of figures and letters: HC PL 2 400                      HC PL 2730</li> <li>or</li> <li>— other identification markings relating to devices complying with the abovementioned description</li> </ul>	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8545 90 90	*01	Cell and battery carbon, in the form of rods, with a length of 34 mm or more but not exceeding 160 mm and a diameter not exceeding 12 mm	0
ex 8548 00 00	*91	Clock/calendar circuit, consisting of a lithium battery, a quartz oscillator and a monolithic integrated circuit of C-MOS technology comprising a programmable generator for periodic interruptions and square waves and a static random-access memory (S-RAM), the whole contained in a housing the exterior dimensions of which do not exceed 18 x 34 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: DS 1287                      DS 1387 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8548 00 00	*92	Contact image sensor, comprising a row of 1 728 or more but not more than 2 050 photosensitive dots and a row of light-emitting diodes (LEDs), contained in a housing the exterior dimensions of which do not exceed 23 x 24 x 277 mm	0
ex 9002 11 00	*40	Auto iris lens, with or without zoom function, fitted with one or more connection cable(s), for use in the manufacture of charge-coupled image observation cameras (a)	0
ex 9021 30 90	*21	Vascular protheses, neither woven nor knitted, of which the largest opening has an internal diameter not exceeding 6 mm	3,1
ex 9021 30 90	*29	Vascular prothese, neither woven nor knitted, of which the largest opening has an internal diameter of more than 6 mm but not exceeding 8 mm	0

(a) Control of the use for this special purpose shall be carried out pursuant to the relevant Community provisions.