

SCHEDULE 6

Regulation 2

Computing hardware

Interpretation

1. In this Schedule—

“computer processing unit” means—

- (a) a central processing unit (also referred to as “CPU”);
- (b) a field programmable gate array (also referred to as “FPGA”);
- (c) a microcontroller;
- (d) a system on chip;
- (e) a graphics processor unit; or
- (f) a specialist processor for artificial intelligence applications;

“fabrication” means the process of producing a microelectronic circuit on a semiconductor substrate or using other advanced materials;

“packaging” means the process of turning a microelectronic circuit on an appropriate substrate into a package suitable for use in an electronic circuit but does not include the assembly and packaging of chips and devices into circuit boards;

“roots of trust” means hardware, firmware or software components that are inherently trusted to perform critical security functions.

Activity – computing hardware

2. A qualifying entity whose activities consist of one or more of the activities set out in paragraph 3.

3. The activities referred to in paragraph 2 are—

- (a) the ownership, creation, supply or exploitation of intellectual property relating to any of the following—
 - (i) computer processing units;
 - (ii) architectural, logical or physical designs for such units;
 - (iii) the instruction set architecture for such units;
 - (iv) code, written in a low-level language, that can control how such units operate;
 - (v) integrated circuits with the purpose of providing memory;
- (b) the design, maintenance or delivery of a service for the secure provisioning or management of either or both of the following—
 - (i) roots of trust of computer processing units;
 - (ii) code, written in a low level language, that can control how such units operate;
- (c) the fabrication or packaging of either or both of the following—
 - (i) computer processing units;
 - (ii) integrated circuits with the purpose of providing memory.