SCHEDULE 6

Computing hardware

Activity – computing hardware

3. The activities referred to in paragraph 2 are—

(a) the ownership, creation, supply or exploitation of intellectual property relating to any of the following—

(i) computer processing units;

(ii) architectural, logical or physical designs for such units;

(iii) the instruction set architecture for such units;

(iv) code, written in a low-level language, that can control how such units operate;

(v) integrated circuits with the purpose of providing memory;

(b) the design, maintenance or delivery of a service for the secure provisioning or management of either or both of the following—

(i) roots of trust of computer processing units;

(ii) code, written in a low level language, that can control how such units operate;

(c) the fabrication or packaging of either or both of the following—

(i) computer processing units;

(ii) integrated circuits with the purpose of providing memory.